# **Test of Step-up Converter Pulse**

## **STEP-UP Voltage Control PWM**

### **Condition**

1. Control the voltage value by adjusting the pulse width.

### **Description**



**STEP UP VOLTAGE Description**

1. T1: Step-up voltage rising time
2. T2: Voltage keeping Time
3. T3: Step-up voltage falling time

**T2 Description**

1. A: Upper Range Value >> 42.0v
2. B: Target Value >> 40.0v
3. C: Lower Range Value >> 38.0v
4. EFFECT RANGE: 1.4v

## **Test Conditions**

### **Target**

1. Search how to implement STEP-UP Voltage Control PWM.
2. Compare with features of implementation methods

### **Hardware Conditions**

1. STM32 NUCLEO-L412KB. MCU model is STM32L412KBU6
2. System clock is 80MHz, timer counter resolution is 1 MHz (prescaler is 80)

### **Software Conditions**

* STM32CubeIDE Version: 1.10.1

### **References**

1. RM0394 Reference manual (STM32L41xxx/42xxx/43xxx/44xxx/45xxx/46xxx advanced Arm®-based 32-bit MCUs)
2. STM32L412xx datasheet
3. STM32 NUCLEO-L412RB-P schematic
4. User manual STM32 NUCLEO-L412RB-P

## **Method 1**

### **Concepts**



1. PWM1: TIM2 CH1
2. OC2: Output compare of TIM2 CH2. Pulse could not be pulse in really.
3. PWM2: TIM1 CH1
4. TIM1 and TIM2 are synchronized timers, TIM2 is master and TIM1 is slave. Trigger of TIM1 start is OC2 matching

### **Description**

1. TIM1 and TIM2 are independent
2. PWM1 and PWM2 are configure as the same conditions (frequency and duty ratio)
3. Matching time of OC2 is w1 + d
4. Delay is handled by HW

## **Comparison of Methods**

### **Pros and Cons**

|  |  |  |
| --- | --- | --- |
| **Method** | **Advantage** | **Disadvantage** |
| 1 | 1. Controlled by HW | 1. Settings are complex and not intuitive  2. Pulse width and delay cannot be changed during PMWs run. |
| 2 | 1. Concept is simple | 1. Pulse width and delay cannot be changed during PMWs run.  2. Controlled by SW, so process delay must be considered |
| 3 | 1. Pulse width or delay can be changed during PMWs run.  2. Controlled by SW, but process delay can be ignored | 1. Concept is complex and not intuitive  2. Deep consideration is required to handle process  3. Two interrupts are required every period of PWM |
| 4 | 1. Pulse width or delay can be changed during PMWs run. | 1. Concept is complex and not intuitive  2. Deep consideration is required to handle process |

### **Resource Required**

|  |  |
| --- | --- |
| **Method** | **Resources Required** |
| 1 | 2 Timers and 2 GPIOs |
| 2 | 2 Timers and 2 GPIOs |
| 3 | 1 Timer and 2 GPIOs |
| 4 | 1 Timer and 2 GPIOs |